CORRECTNESS OF TWO COMPILERS FOR A LISP SUBSET

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ABSTRACT: Using mainly structural induction, proofs of correctness of each of two running Lisp compilers for the PDP-10 computer are given. Included are the rationale for presenting these proofs, a discussion of the proofs, and the changes needed to the second compiler to complete its Proof.

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INTRODUCTION AND JUSTIFICATION

This paper contains proofs of correctness of each of two useful, running compilers, named CO and C4. The source language for both compilers is the same subset of pure (basic) Lisp, which subset excludes special or global variables, function names as arguments, and the form label: the object language is essentially assembly code for the PDP-10 computer; and the compilers themselves are written recursively in RLISP (Haarn 1970), a version of Lisp with Algol-like syntax. The compilers were written by John McCarthy as part of a series of progressively more optimizing compilers for use in a course at Stanford entitled "Computing with Symbolic Expressions." Only later have these compilers been considered for proving correctness. A listing of the compilers and sample output are in the Appendices.

The proof P4 of correctness of the compiler C4 is a modification and extension of the proof P0 for CO. The organization of this paper is first to prove CO correct exclusively. A brief discussion of the proof appears just after the proof, Then using the same machinery that is defined, and using much of the proof PO, the compiler C4 is proved correct. This serial organization, reflecting the essential chronology of the work, seems preferable to proving the two compilers in parallel. The reader should now ignore C4 (and P4) until the start of P4, except to note that the input and overall statement of correctness are the same as for CO.

To prove the correctness of a compiler is a frequently heard challenge. The present proof partly responds to the challenge: The compiler is sufficiently lengthy and complex not to be viewed as merely another cooked-up research example. As evidence of this, Whitfield Diffie has shown the compiler capable of compiling itself successfully. Yet the compiler has certain toy-problem aspects, for example accepting a subset of full Lisp, the inefficiency of the resulting object code, anti the simple parser, it is certainly not a production compiler. Nevertheless, exhibiting yet another proof seems justified since: (i) a compiler is somewhat different from other algorithms that have been proved (there are at least two programs being executed, the compiler and the object program, and, to a lesser extent, the source program); (ii) there has been little progress in proving compilers correct beyond the work of McCarthy & Painter (1967), Painter (1967), Kaplan (1967), Burstall (1969), and Burstall & Landin (1969), although the work of McGowan (1971) should be mentioned; (iii) there remains the worthwhile goal of being able to prove compilers correct; (iv) this proof has been made to serve as the nucleus of a proof of correctness of a more optimizing compiler in the existing series; (v) the informal proof serves as the basis of more formalized proofs, the latter being necessary if a proof of
correctness is to be checked by a proof checker (Milner 1972); and (vi) the correctness of the compiler is not immediately obvious.

THE PROBLEM STATEMENT, NOTATION, AND PLAN OF ATTACK

The reader is assumed to have a basic knowledge of Lisp, say from Weissman's (1967) primer. The input to the compiler is \((\text{DE NAME (args) body})\), \(\text{DE}\) is for Define Expression and \(\text{NAME}\) is the name of the function being compiled. The quantity \((\text{args})\) is the list of arguments (formal parameters) for the function \(\text{NAME}\) and \(\text{body}\) is the body of the function. The calling convention is that a defined function \(f\) of \(N \geq 0\) arguments, say \(\text{arg}_1, \text{arg}_2, \ldots, \text{arg}_N\), will find run-time values of those arguments in successive accumulators starting in \(\text{acl}\), which holds \(\text{arg}_1\), and the result \(f(\text{arg}_1, \text{arg}_2, \ldots, \text{arg}_N)\) will be returned in \(\text{acl}\). This convention applies also to any function call compiled by the compiler in response to a call in the source code, e.g. the call to \(\text{CAR}\) in \(\text{WE SIMPLE (x) (CAR X)}\). In particular the call may be a recursive call, e.g.

\[(\text{DE COMPLEX (X Y) (COND ((NULL X) (CONS Y X)) (T (COMPLEX (CDR X) Y))})\).

We now give a more detailed and more precise description of the allowable syntax and its intended meaning. The list \((\text{args})\) is a list of atoms excepting \(\text{NIL}, \text{T},\) and numeric-atoms; \(\text{body}\) is an expression where expression is defined recursively below \((N \geq 0\) in all relevant cases). The value of an expression \(\text{EXP}\), denoted \(V_{\text{EXP}}\), is recursively defined at the same time (as an "informalization" of the Lisp EVAL function),

(i) \(\text{atom, in particular NIL, T, or a numeric-atom}, \ V_{\text{atom}}:\)
\(V_\text{NIL} = (\text{QUOTE NIL})\) [0 in this compiler],
\(V_\text{T} = (\text{QUOTE T}),\) where a non-NIL value is considered equal to \(V_\text{T},\)
\(V_{\text{numeric-atom}} = (\text{QUOTE numeric-atom}),\) and
\(V_{\text{other atom}} = \text{its binding, i.e., run-time value which may not be a function name,}\)

(ii) \((\text{AND EXP1 EXP2 ..., EXPN}), \ V_{\text{AND-expression}} = \text{T}\) if all \(V_{\text{EXP}}\) are non-NIL otherwise NIL. \(V_{\text{(AND)}} = \text{T},\) AND evaluates its arguments from left to right until either \(\text{NIL}\) is found in which case the remaining arguments are not evaluated, or until the last argument is evaluated,

(iii) \((\text{OR EXP1 EXP2 ..., EXPN}), \ V_{\text{OR-expression}} = \text{T}\) if any \(V_{\text{EXP}}\) is non-NIL otherwise NIL. \(V_{\text{(OR)}} = \text{NIL},\) OR evaluates its arguments from left to right until all non-NILs are found in which case the remaining arguments are not evaluated, or until the last argument is evaluated,

(iv) \((\text{NOT EXP}), V_{\text{NOT-expression}} = \text{T}\) if \(V_{\text{EXP}}\) is NIL otherwise NIL.
(v) \((\text{COND (EXP}_1 \text{ EXP}_2) (\text{EXP}_3 \text{ EXP}_4) \ldots (\text{EXP}_{[2N-1]} \text{ EXP}_{[2N]}))\),

\(\text{V COND-expression is determined as follows. The expressions EXP}_1, \text{ EXP}_3, \ldots, \text{EXP}_{[2N-1]} \) are evaluated starting with EXP}_1 until the first EXP}_{[2I-1]} is found whose value is non-NIL. V COND-expression is then V EXP}_{[2I]}. If no EXP}_{[2I-1]} exists with non-NIL value, then V COND-expression is undefined.

(vi) \((\text{QUOTE EXP})\), V QUOTE-expression = EXP, i.e., EXP unevaluated,

(vii) \((\text{fname EXP}_1 \text{ EXP}_2 \ldots, \text{EXP}_N)\) where \(\text{fname} \neq \text{AND, OR, NOT, COND, QUOTE, V function-expression} = \text{fname(V EXP}_1, \text{V EXP}_2, \ldots, \text{V EXP}_N)\), i.e., the value of the function \(\text{fname} \) applied to its evaluated arguments \(\text{V EXP}_1, \text{V EXP}_2, \ldots, \text{V EXP}_N\). The arguments are evaluated once before the function is called.

(viii) \((\text{LAMBDA (atom}_1 \text{ atom}_2 \ldots, \text{atom}_N) \text{EXP}) \text{EXP}_1 \text{ EXP}_2 \ldots, \text{EXP}_N\)

where \(\text{atom}_1 \neq \text{NIL, T, numeric-atom} \), V LAMBDA-expression is determined as follows. A LAMBDA-expression defines a function which has no explicit (atomic) name, V LAMBDA-expression is the value of this function applied to its evaluated arguments \(\text{V EXP}_1, \text{V EXP}_2, \ldots, \text{V EXP}_N\). In other words, V LAMBDA-expression = V EXP where V EXP is computed after the substitutions \(\text{atom}_1 \leftrightarrow \text{V EXP}_1, \text{atom}_2 \leftrightarrow \text{V EXP}_2, \ldots, \text{atom}_N \leftrightarrow \text{V EXP}_N\) have been made in EXP. If there is a clash of bound variables, the convention is that the innermost binding governs.

Since function names are forbidden as arguments, the expression \((\text{LAMBDA (X) (X)}(Y))\) means a call to the function \(X\) of no arguments rather than a call to the function argument \(Y\). The above syntax forbids \((X), ((X)), \ldots\), etc., as expressions.

The compiler is proved correct under the assumption that its input is syntactically correct. Since no error checking is done by the compiler, nothing is claimed for the results, if any, of incorrect input. Correct input also means, for example, that a list of formal parameters consists of distinct atoms and that the number of formal Parameters is always equal to the number of actual parameters. There are presumably many other such conditions, violations of some of which may have reasonable interpretations.

The statement of correctness of the compiler is that the compiler-produced object code, when executed, leaves a result in acl equal to the value of the source language function applied to the same arguments. The object code takes its N arguments from the accumulators acl, ... acl\(_N\), If \(A = a_1 a_2 \ldots a_N\) represents the arguments, then the correctness statement may be restated as requiring that the equation

\(\text{V ((DE NAME (args) body) A)} = \text{contents Of acl}\)
holds after executing the list of compiler-produced instructions

\[ \text{COMP}(\text{NAME}, (\text{args}), \text{body}) \]

starting with aci holding al for \( 1 \leq i \leq N \).

The following facts about the PDP-10 computer are from a written by McCarthy: The PDP-10 has a 36 bit word and an 18 bit address. In instructions and in accumulators used as index registers this is the right part of the word where the least significant bits in arithmetic reside.

There are 16 general registers which serve simultaneously as accumulators (receiving the results of arithmetic operations), index registers (modifying the nominal addresses of instructions to form effective addresses), and as the first 16 registers of memory (if the effective address of an instruction is less than 16, then the instruction uses the corresponding general register as its operand).

All instructions have the same format and are written for the LAP assembler, program in the form

\[(\text{<op name>} \ <\text{accumulator}> \ <\text{address}> \ <\text{index register}>)\]

Thus (MOVE 13 P) causes accumulator 1 to receive the contents of a memory register whose address is \( 3 + c(P) \), i.e., \( 3 + \text{<the contents of general register P>} \). In the following description of instructions, \( \text{<ef>} \) denotes the effective address of an instruction.

\begin{align*}
\text{MOVE} & \quad c(ac) \rightarrow c(\text{<ef>}) \\
\text{MOVEI} & \quad c(ac) \leftarrow \text{<ef>} \\
\text{HLR} & \quad (\text{used in C4 only}) \quad c(\text{left half of } ac) \rightarrow \text{right half of } c(\text{<ef>}) \\
\text{HRR} & \quad (\text{used in C4 only}) \quad c(\text{right half of } ac) \rightarrow \text{right half of } c(\text{<ef>}) \\
\text{SUB} & \quad c(ac) \leftarrow c(ac) - c(\text{<ef>}) \\
\text{JRS} & \quad \text{go to } \text{<ef>} \\
\text{JUMPE} & \quad \text{if } c(ac) = 0 \text{ then go to } \text{<ef>} \\
\text{JUMPN} & \quad \text{if } c(ac) \neq 0 \text{ then go to } \text{<ef>} \\
\text{CALE} & \quad (\text{used in C4 only}) \quad \text{if } c(ac) = c(\text{<ef>}) \text{ then skip next instruction} \\
\text{CAHN} & \quad (\text{used in C4 only}) \quad \text{if } c(ac) \neq c(\text{<ef>}) \text{ then skip next instruction} \\
\text{PUSH} & \quad \text{c(right half of ac)} \leftarrow c(\text{<ef>}); \text{the contents of each half of ac is increased by one} \\
\text{POPJ} & \quad (\text{POPJP}) \text{ is used to return from a subroutine}
\end{align*}

These instructions are adequate for compiling basic Lisp code with the addition of the subroutine calling pseudo-instruction, (CALL n (E <subr>)) is used for calling the Lisp subroutine <subr> with n arguments. The convention is that the arguments will be stored in successive accumulators beginning with accumulator 1, and the result will be returned in accumulator 1, In particular the functions ATOM and CONS are called with (CALL 1 (E ATOM)) and (CALL 2 (E CONS)) respectively. Note that the instruction (SUB P (C 0 0 3 3)) just deletes the top three elements of the stack P, (PUSH P ac) is used
to put \( c(ac) \) on the stack \( P \). This ends the facts about the PDP-10 computer.

To show the result and effect of executing a section of assembly code, notation of hand-simulation, desk-checking, or tracing of code is used. It is best explained by example. Starting with \( N \) accumulators each holding a value and an empty stack \( P \), namely

\[
\begin{align*}
ac_1 & | a_1 \\
ac_2 & | a_2 \\
\vdots & \ \\
ac_N & | a_N \\
& | P
\end{align*}
\]

the list of instructions

\[
\begin{align*}
((\text{Instructions to leave } a_1 \text{ in } ac_1) \\
(PUSH \ P1) \\
\vdots \\
((\text{Instructions to leave } a_N \text{ in } ac_1) \\
(PUSH \ P1) \\
(MOVE \ 11-N \ P) \\
(MOVE \ 22-h \ P) \\
\vdots \\
(MOVE \ N0 \ P) \\
(SUB \ P\ (C00 NN)) \\
(CALL \ N\ (E\ name))
\end{align*}
\]

gives the trace

\[
\begin{align*}
ac_1 & | a_1* \ al* \ a_2* \ \ldots \ a_N* \ a_1*\ name(a1 \ a2 \ \ldots \ aN) \\
ac_2 & | a_2* \ a_2* \ \ldots \ undsf \\
\vdots & \ \\
ac_N & | a_N* \ a_N* \ undsf \\
& | P| a_1* \ a_2* \ \ldots \ a_N* \\
\end{align*}
\]

Thus the value \( name(a1 a2 \ldots aN) \) is in \( ac_1 \), undsf (an undefined quantity) is in \( ac_i \) for \( 2 \leq i \leq N \) since these accumulators are unsafe over name, and the stack \( P \) is unaltered from the start. The trace shows the final result of tracing; the intermediate results are recorded but marked by an asterisk (*) as being no longer present.

The plan of attack is as follows:

(i) Prove correct 3 auxiliary procedures \( \text{MKPUSH}(N,M), \text{PRUP}(\text{VARS},N), \) and \( \text{LOADAC}(N,K) \) which are not part of the main recursiveness of the compiler (lemmas 1-3),

(ii) under the assumption of no conditional expressions or Boolean expressions (i.e., no \text{COND}, \text{AND}, \text{OR}, \text{NOT}), prove the compiler correct (theorems 1-3 and termination), and

(iii) Prove the compiler correct without the restrictive assumption
of (ii) (theorems 4-7).

The proof techniques to be used are mainly those shown in London (1970). The factorization into (ii) and (iii), convenient for constructing algorithms, for presenting, and for reading the proof, shows how one can prove an algorithm in suitable steps rather than having to do it all at once. If the reader omits theorems 4-7 of (iii), the proof of correctness of an interesting subcompiler results. In this part recursion is still allowed in the sense that the compiler will correctly compile a recursive function, but the object code may not terminate if such a recursive function is called, since there is no branching to "stop the recursion?"

The numbering of the lemmas and theorems reflects the order of their discovery and proof. The order could be altered by merging theorems 1 and 7 and by placing theorem 3 as the last theorem if the sole interest were to prove the entire compiler.

**PROOF OF AUXILIARY FUNCTIONS FOR C0**

The LISP operation **CONS** is denoted in RPLISP by an **infix** dot() function:

\[ A,3 = (\text{CONS } A B) \]

By inspection of the whole compiler, it follows that all numerically-valued quantities are integers, \( * \) is used as an end-of-proof marker.

**Lemma 1.** If \( N > 0 \) and \( M > 0 \), then \( \text{MKPUSH}(N, M) = \)

\[
((\text{PUSH} \ P \ M) \\
(PUSH \ P \ M+1) \\
\ldots \\
(PUSH \ P \ N)),
\]

If \( M > 0 \), then \( \text{MKPUSH}(0, M) = \text{NIL} \).

Proof. Backwards induction on \( M \). If \( M > N \), \( \text{MKPUSH}(N, M) = \text{NIL} \).

If \( M = N \), we have \( (\text{PUSH} \ P \ M), \text{NIL} = ((\text{PUSH} \ P N)) \). Assume the lemma for \( M \leq N \) and consider \( M1 > 0 \).

\[
\text{MKPUSH}(N, M-1) = (\text{PUSH} \ P \ M-1), \text{MKPUSH}(N, M) \text{ since } N > M-1
\]

\[
= (\text{PUSH} \ P \ M-1) \\
((\text{PUSH} \ P \ M) \\
(PUSH \ P \ M+1) \\
\ldots \\
(PUSH \ P \ N)) \text{ by induction hypothesis for } M
\]

\[
= ((\text{PUSH} \ P \ M-1) \\
(PUSH \ P \ M) \\
(PUSH \ P \ M+1) \\
\ldots \\
(PUSH \ P \ N)) \text{ by definition of CONS, *}
\]
Alternative notation may be used to avoid the three dots (...,) in the lemma and in the proof. Analogously to the \( \sigma \) notation for indicating \( S \)-urns (e.g., \( \sigma(I=1,N,W) \)), define a list functional \( L \) as follows:

\[
L(i=M,N,(PUSH P I)) = \text{NIL} \quad \text{if } N < M
\]
\[
L(i=M,N,(PUSH P I)) = (PUSH P M), L(i=M+1,N,(PUSH P I)) \quad \text{if } N \geq M
\]

Whereas \( \sigma \) denotes iterated addition, \( L \) denotes iterated \textsc{consing}.

The lemma is restated as \( \text{MKPUSH}(N,M) = L(i=M,N,(PUSH P I)) \). The proof of the induction step becomes:

\[
\text{MKPUSH}(N,M-1) = (PUSH P M-1), \text{MKPUSH}(N,M)
\]
\[
= (PUSH P M-1), L(i=M,N,(PUSH P I))
\]
\[
= L(i=M-1,N,(PUSH P I)).
\]

Similar notation may be used for lemmas 2 and 3 below.

**Lemma 2.** Let \( \text{VARS} = (x_1 x_2 \ldots x_M) \). Then \( \text{PRUP}(\text{VARS}, N) = ((x_1,N) (x_2,N+1) \ldots (x_M,N+M-1)) \). This list of pairs is called the \( \text{PRUP} \) list, short for "pair-up."

**Proof.** Induction on \( M \). If \( M = 0 \), then \( \text{PRUP}(\text{VARS}, N) = \text{NIL} \) since \( \text{NULL VARS} \). Assume for \( M \geq 0 \) and consider \( M+1 \).

\[
\text{PRUP}(\text{VARS}, N) = (\text{CAR VARS}, N), \text{PRUP}(\text{CDR VARS}, N+1)
\]
\[
= (x_1,N), ((x_2,N+1) \ldots (x[M+1],N+M)) \quad \text{by the induction hypothesis for } \text{CDR VARS}
\]
\[
= ((x_1,N) (x_2,N+1) \ldots (x[M+1],N+M)) \quad \text{by use of } \ldots \text{.}
\]

**Lemma 3.** \( \text{LOADAC}(N,K) = ((\text{MOVE} K N P) (\text{MOVE} K+1 N+1 P) \ldots (\text{MOVE} K+N 0 P)) \).

**Proof.** Backwards induction on \( N \). If \( N > 0 \), the result is \( \text{NIL} \). If \( N = 0 \), we have \( (\text{MOVE} K 0 P), \text{NIL} = ((\text{MOVE} K-0 0 P)), \) Assume the lemma for \( N < 0 \) and consider \( -N+1 \).

\[
\text{LOADAC}(N-1,K) = (\text{MOVE} K N-1 P), \text{LOADAC}(N,K+1) \text{ since } N-1 < 0
\]
\[
= (\text{MOVE} K N-1 P), ((\text{MOVE} K+1 N P) \ldots (\text{MOVE} K+N 0 P)) \quad \text{by induction hypothesis for } N
THE RUN-TIME STACK

The object code uses a run-time stack in a rather standard way for holding the actual parameters of both function calls and LAMBDA expression evaluations. As each actual parameter (binding) is evaluated, it is pushed onto the stack. This suffices for a LAMBDA expression but not for a function. After all of the latter's actual parameters are evaluated and pushed onto the stack, all are moved to the accumulators and popped from the stack in order to satisfy the conventions for calling a function. The first task of the compiled function definition is to push the actual parameters back to the stack from the accumulators. Thus for both a function and a LAMBDA expression, the respective code body accesses or obtains the actual parameter from the stack.

We forgo stating the various possible stack configurations in full generality to avoid (presumably) less than transparent notation. What is in principle required can be seen by an example:

\[(\text{DEF } (A, B) (G \ A ((\text{LAMBDA}(A, (\text{CAR } A)) B) A) B))\]

This must be compiled identically to

\[(\text{DEF } (A, B) (G \ A ((\text{LAMBDA}(\text{A1}, (\text{CAR } A1)) B) A) B))\]

where the bound \(A\) of the LAMBDA expression has been renamed \(A1\). The accessible variables of \(F\) are \(A\) and \(B\); those of the LAMBDA expression are \(A1\) and \(B\). At the point of compiling the argument \(A\) of \(\text{CAR } A\), the stack \(P\) (at run-time) will be

\[
\begin{array}{ccc}
A & B & A & B \\
\text{actual} & \text{the first actual parameter} & \text{parameters} & \text{actual parameter corresponding} \\
\text{to the call} & \text{to the call of } G & \text{to } A1 \\
\text{of } F & & & \\
\end{array}
\]

The compile-time PRUP list will be \(((A, 4), (A, 1), (B, 2))\) or, using \(A1\), \(((A1, 4), (A, 1), (B, 2))\). Note the absence of a \(3\) since that spot holds a temporary value and not the value of an actual parameter usable in the body of the LAMBDA expression (in this example either \(A1\) or \(B\) but not \(A\)).

Thus the compilation of the argument \(A\) of \(\text{CAR } A\) (at case 3 of \ COMPEXP with \(M = -4\) as it would be) produces a MOVE involving the top of the stack, namely \((\text{MOVE } 1 \ M+4 \ P) = (\text{MOVE } 1 \ 0 \ P)\), and not \((\text{MOVE } 1 \ M+1 \ P) = (\text{MOVE } 1 \ -3 \ P)\). A compilation of \(B\) at this point would produce \((\text{MOVE } 1 \ M+2 \ P) = (\text{MOVE } 1 \ -2 \ P)\).
After compiling the fourth, and last, actual parameter of G, the stack will be

\[ \text{A B} \quad \text{A C A R B A B} \]

actual parameters  actual parameters
to the call of F  to the call of G

We shall need to show that the proper run-time stack configuration is set up and maintained, and that the quantity M and the integers in the PRUP list together produce the correct accessing from the stack P. The quantity -M gives the number of stack locations currently accessible by the function being compiled. Let us define the predicate STACKOK(M,PRUP) to mean (1) M is the correct number of stack locations, and (2) M and the integers in the PRUP list at compile-time together produce the correct accessing of the stack at run-time. The definition of STACKOK includes the representation of "what the compiler knows so far" concerning the location in the stack of variables and temporary values, As part of no error checking the compiler assumes an infinite run-time stack with no tests for stack overflow. The proof accordingly makes the same assumption.

**PROOF OF THE MAIN THEOREMS FOR C0**

The main proof technique used for theorems 1, 2, and 4-7 is structural induction on expressions. Each theorem states what a procedure of the compiler does: theorems 1 and 7 for COMPEXP, 2 for COMPLIS, 4 for COMPANDOR, 5 for COMBOOL, and 6 for COMCONO. Each of these procedures is recursive and also can call many of the other procedures. To prove these theorems for an arbitrary expression EXP, the following induction hypothesis is used for each theorem: Theorems 1, 2, and 4-7 have all been proved for all subexpressions of EXP. To invoke one of these theorems inductively on a subexpression, it is necessary to verify that all hypotheses of that theorem are satisfied.

The length of the list X will be denoted by LX. All procedures of the compiler except for PRUP produce as values a list of compiled instructions, as may be verified by inspection (in particular noting that each one-line code generation is a one-element list and otherwise the APPEND function is used). The quantities VPR and M, which appear as actual parameters to the procedures in the theorems 1, 2, and 4-7, are unchanged by these procedures in view of the definition of functional evaluation.

Theorem 1 (Definition of COMPEXP(EXP,M,VPR)). Assume the following conditions hold at the call of COMPEXP(EXP,M,VPR):

\( C1: \) EXP is an expression.
\( C2: \) M = 0 and -M is the number of stack locations currently accessible by the function being compiled.
Variables currently accessible to EXP are X₁, X₂, ..., Xₖ with 
K ≤ M.

vPR is a PR list of K pairs (X₁, J), 1 ≤ J ≤ M, of the currently 
accessible variables where the innermost occurrence (of a formal 
parameter) of a duplicated variable name appears first on vPR, 
e.g., ((L, 7) (B, 8) (D, 6) (A, 1) (B, 2) (C, 3)).

At run-time the stack P contains the values of the variables and 
temporary values as 
P|X₁ X₂ ... X[M]
where X[-M] is at the top of the stack.

EXP is an atom (≠ NIL, ≠ T, ≠ numeric-atom) ⇒ EXP is a variable X₁, 
1 ≤ 1 ≤ K, on the vPR list.

Proof of definition of COMPEXP (under the assumption of no 
conditional or Boolean expressions; theorem 7 proves COMPEXP with 
such expressions), Structural Induction on EXP. Basis step: EXP is 
an atom, either NIL, T, a numeric-atom, or other atom. If EXP is 
NIL, then case 1 of COMPEXP produces ((MOVE 1 0)) so acl holds 0 = 
V NIL. If EXP is T, then case 2 produces ((MOVE 1 (QUOTE T))) so acl 
holds (QUOTE) = VT. If EXP is a numeric-atom then case 2 
produces ((MOVE 1 (QUOTE numeric-atom))) so acl holds (QUOTE 
numeric-atom), the correct value. If EXP is an other atom, than case 
3 produces ((MOVE 1 M+C DR ASSOC(EXP, vPR) P)). By C7 let EXP = X₁ 
appear first on vPR in the pair (X₁, J). By C4 CDR ASSOC(EXP, vPR) = 
CDR (X₁, J) = J. By C5 and C6 the instruction (MOVE 1 M+J P) loads 
ac1 with V X₁. Note 1 ≤ J ≤ M ≥ M+15M+JS0, i.e., a valid stack access.

Induction step: CAR EXP and CDR EXP are always defined at cases 
4-7 (a total of 10 occurrences) since NOT ATOM EXP because case 3 
failed. If EXP = (QUOTE a), then case 6 is the first to hold 
producing ((MOVE 1 (QUOTE a))) as required.

If EXP = (fname a) with fname not one of AND, OR, NOT, COND, 
QUOTE, then case 7 is the first to hold, EXP thus is a (non-special) 
function to be evaluated using arguments of the list α = (a₁ a₂ ... 
αₙ) where N = L α ≥ 0. The list of instructions produced is

((COMPLIS(α), M, VPR))
(LOADAC(1-N, 1))
(SUB P CC 0 0 N N))
(CALL N (E fname)))

Conditions D₁-D₇ (see theorem 2) for Inductively Invoking COMPLIS 
hold as follows:

10
Using the definitions of COMPLIS and LOADAC, we obtain

---

((Instructions to leave V α1 in ac1)
(PUSH P 1)
COMPLIS
... (Instructions to leave V αN in ac1)
(PUSH P 1)
(MOVE 1 1-N P)
LOADAC
(MOVE 2 2-N P)
---
(MOVE N 0 P)
(SUB P (C 0 0 N N))
(CALL N (E fname))

Tracing these instructions, namely

\[ \text{ ac1|α1* α1* α2* ... αN* α1* fname(V α1, V α2, ..., V αN) } \]
\[ \text{ ac2|α2* α2* undef } \]
\[ \text{ ... } \]
\[ \text{ acN|αN* αN* undef } \]
\[ \text{ P|α1* α2* ... αN* } \]

gives the desired result (including the case N=0) since \( V \text{ EXP} = \text{fname}(V α1, V α2, ..., V αN) \). Note that the Instruction (CALL N (E fname)) may be a recursive call since the standard conventions of arguments and returned value are obeyed, and the arguments are stacked (saved) by the called function. Recall that function names are forbidden as arguments so a formal parameter name may be called by a CALL Instruction.

Finally if \( \text{EXP} = ((\text{LAMBDA} (α) β) ε) \), then only case 8 holds. Since case 7 fails, NOT ATOM CAR EXP. Let \( N = L \ ε = L α \) by correct input. The list of instructions produced is

\[ (((\text{COMPLIS}(ε), M, VPR)) \]
\[ (((\text{COMPEXP}(β, M-N, APPEND(PRUP((α), 1-M)), VPR)))) \]
\[ (((\text{SUB} P (C 0 2 N N))) \]

Conditions D1-D7 for inductively invoking COMPLIS hold as follows:

D1: Definition of (ε), D2: C2, D3: C3 on (ε), a subpart of EXP,
D4, D5, D6: C4, C5, C6, respectively, D7: Syntactically correct input.

Conditions C1-C7 for inductively invoking COMPEXP hold as follows:
C1: $\beta$ is an expression by the syntax definition involving \textsc{lambda}.
C2: $M-N \leq 0$ since $M \leq 0$ and $N \geq 0$. There are now $-(M-N) = -M+N$ stack locations currently accessible.
C3: Variables currently accessible are $x_1 x_2, \ldots, x_{K+N}$, i.e. there are now $K+N$ variables allowed in $\beta$, $K+N \leq -M+N$ since $K \leq -M$
C4: Definition of PRUP and C4,C5, and C6 applied to $VPR$. The new pairs are put first. The new indices are $1-M = -M+1$ through $-M+N$.
C5: C5 for $X1, \ldots, X[-M]$, together with COMPLIS((\epsilon), M; VPR)) for $X[-M+1], \ldots, X[-M+N]$.
C6: C6, C4 just above, and C5 just above.
C7: Syntactically correct input and the augmented PRUP list.

Hence tracing these instructions, namely

\[ \text{ac1}|X[-M+1]*, \ldots, X[-M+N]* ; V \text{ EXP} \]
\[ P|X1X2, \ldots, X[-M] X[-M+1]*, \ldots, X[-M+N]* \]

gives the desired result (including the case $N = \emptyset$), since COMPLIS essentially makes the substitutions at $= \nu \epsilon$ and then COMPEXP computes $V_\beta$ which is now $V_\text{EXP}$.

In all cases the stack $P$ is safe over the execution of $I$. Note that $VPR$ remains unaltered even in the \textsc{lambda} case because here the augmented PRUP list in the call to COMPEXP is acaply only for that recursive call when that call finishes the outer $VPR$ list is intact.

**Theorem 3** [Definition of COMPLIS($U,M,VPR$)]. Assume the following conditions hold at the call of COMPLIS($U,M,VPR$):

D1: $U = (u1 u2, \ldots, uN)$ is a list of arguments.
D2: COMPEXP's C2.
D3: Variables currently accessible to the members of $U$ are $X1, X2, \ldots, XK$ with $K \leq -M$.
D4, D5, D6: COMPEXP's C4, C5, C6, respectively.
D7: COMPEXP's C7 with EXP replaced by $Uj$.

Result, COMPLIS = (((instructions to leave $V u_1$ in ac1)
(PUSH P 1)

\[
\text{依照...}
\]

Proof of definition of COMPLIS. Structural Induction on $U$.
Basis step: $U$ is NULL whence COMPLIS = NIL. Induction step: Since $U \neq \text{NIL}$, COMPLIS($U,M,VPR$)

\[ = ((\text{COMPEXP}(u1,M,VPR))
(PUSH P 1)
\]

(COMPLIS((u2, ..., uN), M-1,VPR))).

\[12\]
Conditions C1-C7 for inductively invoking COMPEXP hold by D1-D7, respectively. Hence invoking COMPEXP shows

\[
\text{(COMPEXP}(u_1, M, VPR)) = (\text{Instructions to leave } V \text{ u}_1 \text{ in } ac_1)
\]

with the stack P safe, (PUSH P 1) stacks V u1 on the top of P.

Conditions D1-D7 for invoking the induction hypothesis for COMPLIS hold as follows:

D1: By D1 for U,
D2: By D2 and (PUSH P 1) which means there are now \(-(M+1)\) stack locations, the top one being a temporary value,
D3: By D3 \((K \leq -N \rightarrow K \leq -M+1)\),
D4: By D4,
D5: By D5 and (PUSH P 1), P Is \(P|X_1 X_2 \ldots X_{[-M]} V U_1\),
D6: By D6 and D5 just above,
D7: By D7,

Hence the induction hypothesis shows COMPLIS((u2 \ldots uM), M-1; VPR) =

\[
(\text{Instructions to leave } V U_2 \text{ in } ac_1) \\
(PUSH P 1) \\
\ldots \\
(\text{Instructions to leave } V U_N \text{ in } ac_2) \\
(PUSH P 1)
\]

Hence COMPLIS(U, M, VPR) =

\[
(\text{Instructions to leave } V U_1 \text{ in } ac_1) \\
(PUSH P 1) \\
\ldots \\
(\text{Instructions to leave } V U_N \text{ in } ac_1) \\
(PUSH P 1)
\]

Theorem 3 [Correctness of the compiler], Let \(A = a_1 a_2 \ldots a_N\) be an arbitrary list of actual parameters, Starting with ac|holding \(a_1, 1 \leq i \leq N\), and after execution of the list \(I\), of instructions produced by COMP(NAME,(args),body) we have

\[
V((\text{DE NAME (args) body}) A) = \text{contents of } ac_1
\]

and the stack P Is Safe over the execution of \(I\).

Proof, Let \(N = L (\text{args}). \text{COMP}(NAME,(\text{args}),body) =

\[
(\text{LAP NAME SUBR) (MKPUSH(N,1)) (COMPEXP(body,-N,PRUP((args),1))) (SUB P (C \emptyset N N)) (POPP P) N \text{ L})
\]

13
by using the definitions of MKPUSH and COMPEXP although it remains to show that MKPUSH and COMPEXP may be invoked. Since $N \geq 0$ we may invoke MKPUSH. The conditions C1-C7 for COMPEXP hold as follows:

C1: body is an expression by the assumption of syntactically correct input.
C2: $-N = -\text{LENGTH}(\text{args}) \leq 0$, $-N = N$ is the correct number of stack locations since previously $-\text{LENGTH}(\text{args})$ locations are accessible.
C3: the accessible variables are $a_1, a_2, \ldots, a_N$.
C4: By definition of PRUP($(\text{args}), 1)$.
C5: By the number $N$ of $\text{(PUSH P)}$ instructions.
C6: STACKOK($-N, \text{PRUP}$) holds by the definition of PRUP and the order of the PUSH instructions.
C7: By syntactically correct input and the definition of PRUP(VARS, 1).

Thus starting with acf holding $a_i$ for $1 \leq i \leq N$, we have the trace

\[
\begin{align*}
ac_1 | a_1 & \rightarrow V \text{ body} \\
ac_2 | a_2 & \rightarrow \text{ undef} \\
\vdots \\
ac_N | a_N & \rightarrow \text{ undef} \\
P | a_1 & \rightarrow a_2, \ldots, a_N.
\end{align*}
\]

Since $V \text{ body} = ((\text{DE NAME} (\text{args}) \text{ body}) A) and the stack is safe, the result is proved. (If conditional and Boolean expressions are allowed, then theorem 7 is needed.)

**Theorem 4** (Definition of COMPANDOR($U, M, L, FLG, VPR$)). Assume the following conditions hold at the call of COMPANDOR($U, M, L, FLG, VPR$):

E1: $U = (u_1 u_2, \ldots, u_N)$ is a list of Boolean expressions,
E2: COMPEXP's C2,
E3: COMPLIS's D3,
E4, E5, E6: COMPEXP's C4, C5, C6, respectively,
E7: COMPLIS's D7,
E8: L is a label.
E9: FLG is T or NIL,
Result, COMPANDOR produces a list $I$, of instructions given by

\[
\begin{align*}
\text{FLG} & | \text{Algol equivalent of } I \\
\text{NIL} & | \text{if NOT } u_1 \text{ then go to } L; \notag \\
& | \text{if NOT } u_2 \text{ then go to } L; \notag \\
& \cdots \notag \\
\text{T} & | \text{if } u_1 \text{ then go to } L; \notag \\
& | \text{if } u_2 \text{ then go to } L; \notag \\
& \cdots \notag \\
& | \text{if } u_N \text{ then go to } L; \\
\end{align*}
\]

with the statement labeled $L$ not in $I$, $P$ is safe over the execution of $I$.

Proof of definition of COMPANDOR, Structural Induction on $U$,
Basis step: $U$ is NULL whence COMPANDOR = NIL, Induction step:
Assume FLG = T, COMPANDOR($U,M,L,FLG,VPR$)

\[
= (((\text{COMBOOL}(u_1,M,L,FLG,VPR))) \\
(\text{COMPANDOR}((u_2 \cdots u_N),M,L,FLG,VPR))) \notag \\
\text{by definition of COMPANDOR since } U \neq \text{NULL}
\]

\[
= (((\text{if } u_1 \text{ then go to } L;) \\
(\text{COMPANDOR}((u_2 \cdots u_N),M,L,FLG,VPR))) \notag \\
\text{by Inductively Invoking COMBOOL on the Boolean expression} u_1
\]

\[
= (((\text{if } u_1 \text{ then go to } L;) \\
(\text{if } u_2 \text{ then go to } L;) \notag \\
\cdots \notag \\
(\text{if } u_N \text{ then go to } L;)) \notag \\
\text{by Inductively Invoking COMPANDOR} \notag \\
\text{on the list } (u_2 \cdots u_N); \notag \\
E_2-E_7 \text{ hold prior to invoking COMPANDOR since } P \text{ is safe over } \text{"if } u_1 \text{ then go to } L;\text{" and both } M \text{ and } VPR \text{ are unaltered by COMBOOL},
\]

$L$ is in neither the first instruction nor in instructions $2$ through $N$ whence $L$ is outside $I$. Similarly the stack $P$ is safe. The case FLG = NIL is proved similarly.

Theorem 5 [Definition of COMBOOL($P,M,L,FLG,VPR$)]. Assume the following conditions hold at the call of COMBOOL($P,M,L,FLG,VPR$):

$F_1$: $P$ is a Boolean expression,
$F_2-F_7$: COMPEXP's $C_2-C_7$, respectively, with EXP replaced by $P$,
$F_8$: $L$ is a label,
$F_9$: FLG is T or NIL.
Result. COMBOOL produces a list, \( I \), of instructions given by

\[
FLG \mid Algol equivalent of I
\]

\[
\text{NIL \ if \ NOT \ P \ then \ go \ to \ L;}
\]

\[
T \mid \text{if \ P \ then \ go \ to \ L;}
\]

4th the statement labeled \( L \) not in \( I \), \( P \) is safe over the execution of \( I \).

Proof of definition of COMBOOL, Structural Induction on \( P \). Assume \( FLG = T \). Basis step: \( P \) is an atom \( \text{COMBOOL}(P,M,L,FLG,VPR) \)

\[
= (\text{COMPANDOR}(P,M,L,FLG,VPR))
\]

\[
\text{by \ case \ 1 \ of \ COMBOOL}
\]

\[
= ((\text{Instructions to leave \( V \) \) in \( \text{ac1} \))}
\]

\[
\text{(JUMPN 1 L)) \quad \text{by "Inductively" invoking COMPEXP (more}
\]

\[
\text{precisely, by \ repeating on the atom \( P \) the basis}
\]

\[
\text{step of the proof of COMPEXP; Induction is}
\]

\[
\text{Invalid since the \( P \) in \( \text{COMPEXP} \) is not a sub-}
\]

\[
\text{structure of \( P \) in \( \text{COMBOOL} \)).}
\]

\[
= (\text{if \ P \ then \ go \ to \ L;}) \quad \text{by checking 2 cases,}
\]

Induction step: \( \text{CAR} \ P \) and \( \text{CDR} \ P \) are always defined at cases 2-5 since \( \text{NOT} \ \text{ATOM} \ P \) because case 1 failed. Also \( \text{CDR} \ P \) is defined at case 4 since the \( \text{NOT} \) operator must have an argument.

If \( P = (\text{AND} \ a) \), then from case 2b (with \( FLG = T \)) \( \text{COMBOOL} \)

\[
= ((\text{COMPANDOR}(\text{a},M,L1,NIL,VPR)))
\]

\[
(\text{JRST } 0 \ L) \quad \text{[the } 0 \text{ is redundant]}
\]

\[
\text{by \ letting \text{GENSYM()} \ be \ the \ label \( L1 \neq L \}
\]

\[
\text{since \ each \ call \ to \text{GENSYM} \ gives \ a \ unique}
\]

\[
\text{value}
\]

\[
= ((\text{if \ NOT} a_1 \text{ then \ go \ to \ L1}))
\]

\[
(\text{if \ NOT} a_2 \text{ then \ go \ to \ L1})
\]

\[
(\text{if \ NOT} a_n \text{ then \ go \ to \ L1;})
\]

\[
(\text{JRST } 0 \ L)
\]

\[
\text{by \ Inductively \ invoking \text{COMPANDOR} \ on \( (a) \),}
\]

\[
\text{a \ Boolean \ list}
\]

\[
= (\text{if \ P \ then \ go \ to \ L; L1;}) \quad \text{by \ checking \ cases \ that \ define}
\]

\[
\text{AND \ (including \ evaluation \ only \ until \ the}
\]

\[
\text{first} \text{NIL} a \text{ and the case} \ (\text{AND} \) \text{with NULL}
\]

\[
\text{a),}
\]

If \( P = (\text{OR} \ a) \), then from case 3e (with \( FLG = T \)) \( \text{COMBOOL} \)
= (COMPANOR((a),M,L,T,VPR))

= ((if a1 then go to L1)
  (if a2 then go to L1)
  ...
  (if aN then go to L1)) by inductively invoking COMPANOR on (a), a Boolean list

= (if P then go to L1) by checking cases that define OR
   (including evaluation only until the first non-NIL a1 and the case OR with NULL a).

If P = (NOT a1), then from case 4 COMBOOL

= (COMBOOL((a1),M,L,NOT FLG,VPR))

= (if NOT a1 then go to L1) by inductively invoking COMBOOL on (a1), a one-element Boolean list

= (if P then go to L1) by definition of P.

If P is any other Boolean expression, then case 5 yields

((COMPEXP(P,M,VPR))
 (JUMP 1 L1)).

Immediate inductive invoking of COMPEXP is invalid because the P in
COMPEXP is not a substructure of P in COMBOOL. But control's
reaching case 5 of COMBOOL means P is not an atom (case 1) and means
CAR P is neither AND, OR, NOT (cases 2-4). Thus COMPEXP(P,M,VPR) will
be computed by one of its cases 5-8 all of whose procedures are
called with substructures of P. (It is crucial to avoid case 4 of
COMPEXP to avoid the cycle COMBOOL(P...) → COMPEXP(P...) →
COMBOOL(P...),) COMPEXP(P,M,VPR) may be calculated by repeating the
proof of cases 5-8 on P (see thereoms 7 and 1); this yields the same
calculation as the basis step for COMBOOL. Since the definition of
GENSYM guarantees unique labels being generated, the label L is not
in the "Instructions to leave V P in ac1."

The case FLG = NIL is proved similarly.

Theorem 6 [Definition of COMCOND(U,M,L,VPR)], Assume the
following conditions hold at the call of COMCOND(U,M,L,VPR):

G1: U = ((u1 u2) (u3 u4) ... (u[2N-1] u[2N])) is a list of pairs of
expressions, the first of each pair being a Boolean expression,
G2-G7: COMPEXP's C2-C7, respectively, with EXP replaced with uj,
G8: L is a label,

Result. COMCOND gives a list, I, of instructions equivalent to the Algol
acl := if \( u_1 \) then \( u_2 \) else if \( u_3 \) then \( u_4 \) ... else
[\( if[u[2N-1]] \) then \( u[2N] \); L]

\( P \) is safe over the execution of \( I \). If no \( u[2][-1] \) is non-NIL, the value in \( acl \) is undefined. In other words \( acl := V \) COND-expression.

Proof of definition of \( \text{COMCOND} \). Structural induction on \( U \).

Basis step: \( U \) is NULL whence \( \text{COMCOND} \) produces, as required, just the label \( L_1 \). Induction step: \( \text{NOT} \) NULL \( U \) and correct syntax imply \( \text{CAAR} \), \( \text{CADAR} \), \( U \), and \( \text{CDR} \) are always defined. \( \text{COMCOND}(U, M, L, VPR) = (\text{COMBOL}(u_1, M, L_1, NIL, VPR)) - \text{COMPExp}(u_2, M, VPR) \)

(JRST L)

\( L_1 \)

by \( \text{letting GENSYM()} \) be the label \( L_1 \neq L \)

\( = ((\text{IF NOT} u_1 \text{ then go to } L_1)) \)

(Instructions to leave \( V \) \( u_2 \) in \( acl \))

(JRST L)

\( L_1 \)

\( (acl := if u_3 \text{ then } u_4 \ldots \text{ else if } u[2N-1] \text{ then } u[2N] ; L_1) \)

by Inductively Invoking \( \text{COMBOL}, \text{COMPExp}, \) and \( \text{COMCOND} \)

\( = (acl := if u_1 \text{ then } u_2 \ldots \text{ else if } u[2N-1] \text{ then } u[2N] ; L_1) \)

by checking cases involving \( V \) \( u_1 \).

P is safe as required. The case of no \( u[2-1]\) being non-NIL gives an undefined result as required (in particular for \( N = 0 \)), \( \star \)

Theorem 7. \( \text{COMPExp}(\text{EXP}, M, VPR) \) as defined in theorem 1 also holds for conditional and Boolean expressions.

Proof. (An addition to the proof of theorem 1.) Basis step: Vacuous. Induction step: If \( \text{EXP} = (\text{Boolean } a)\) with Boolean one of \( \text{AND}, \text{OR}, \text{NOT}, \) then case 4 is the first to hold, \( \text{COMPExp}(\text{EXP}, M, VPR) = ((\text{COMBOL}(\text{EXP}, M, L_1, NIL, VPR)) \)

(MOVEI 1 (QUOTE T))

(JRST \( \emptyset \) L2)

\( L_1 \)

(MOVEI 1 0)

L2)

where \( L_1 \neq L_2 \) are the two \( \text{GENSYM()} \) labels

\( = ((\text{IF NOT EXP then go to } L_1)) \)

(MOVEI 1 (QUOTE T))

(JRST \( \emptyset \) L2)

\( \text{L1} \)

(MOVEI 1 0)

\( L_2 \)

by repeating the proof of cases 2-4, all
Involving substructures, of COMBOOL(\text{EXP},.) since case 4 of COMPEXP means \text{CAR EXP} is either \text{AND}, \text{OR}, or \text{NOT}.

If \text{V EXP} = \text{T}, then \text{ac1 holds (QUOTE T)} as required since the (MOVEI 1 (QUOTE T)) and the (JRST 0 L2) instructions are executed. If \text{V EXP} = \text{NIL}, then \text{ac1 holds 0} as resulted since control goes to L1 and the (MOVEI 1 0) is executed.

If \text{EXP} = \text{(COND \alpha)}), then case 5 is the first to hold, COMPEXP = COMCOND((\alpha),M,L,VPR) using the label L for GENSYM(), Invoking COMCOND inductively shows the required value, according to the definition of \text{COND, is iac1.}

\text{TERM NATION OF THE COMPILERCO}

Except to COMP in theorem 3, add the statement "and the procedure terminates" to the result of each procedure definition of the compiler. The induction hypothesis will show termination of each procedure call on a substructure. The induction step is now reduced to essentially "straight-line code" which terminates, COMP terminates since MKPUSH and COMPEXP do.

To show that COMBOOL and COMPEXP terminate when one is called from the other on the original structure, We can repeat a proof Part as was done in the proofs of theorems 5 and 7.

\text{DISCUSSION OF THE PROOF P0}

The process of constructing this proof may be viewed as discovering enough of the assumptions about the input and the programming conventions used in writing the compiler, as stating them, and as proving them to be preserved or consistently followed over all the procedures of the compiler. The successful factorization involving conditional and Boolean expressions was useful in doing this. The recursion of the compiler has been handled by the statements of the theorems, including three dots (\ldots) as needed, and by the use of structural induction. In addition, some lessons of top-down programming (Dijkstra 1970), stepwise refinement (Wirth 1971), and Hoare's (1971) approach were applied in the proof process although informally.

It is noteworthy that the proof process uncovered no errors in the compiler. A previous version of this paper omitted completely numeric-atoms although condition C7 (then written without the clause "\# numeric-atom") unintentionally excluded them. Diffie noticed their omission when the compiler aborted while compiling a factorial function. Since numeric-atoms are needed for self-compilation, case 2 of COMPEXP was changed to include numeric-atoms. No other changes were made to the compiler. The previous version of this paper did not exclude the use of NIL, T, and numeric-atoms as formal parameters nor the use of function names as arguments. They must be excluded.
since the compiler fails on these inputs.

Despite the compiler's being written purely functionally, this proof may be usefully viewed as employing inductive assertions. When applied to recursive procedures of the kind in the compiler, the method verifies the conditions necessary for calling a procedure (including a recursive call). The result of the procedure is then used to show what is true after the call (even if the procedures are called merely as arguments to the APPEND function). This is the same way a standard iterative program is proved.

Unexplored so far are the implications for automatic proof checking, of the length Of this informal, but hopefully rigorous proof. Next is the Proof P4.

THE COMPILER C4 AND PROOF OF CORRECTNESS P4

The input to the compiler C4 and the overall statement of correctness are the same as for C0. The compiler C4 is similar in structure to C0, has twice as many lines of code as C0, and produces about half as many instructions for a given function as C0. In response the proof P4 contains eleven new theorems and lemmas (Theorems 6-12 and Lemmas 4-9) corresponding to the eleven new functions in C4. Also P4 contains modifications to the proofs (mainly additional cases) of theorems 1, 3, and 5-7 reflecting the changes in C4 to the functions of C0. The similar structure allows much of the proof P3, without change, to become a part of P4. In particular, the statements of lemmas 1 and 2 and theorems 1-7 are unchanged (LOADAC, the subject of lemma 3, is a completely new function) because the generally more efficient compiled code of C4 accomplishes the same overall effect as does the code of C0. The proofs of the new theorems and the proofs of modifications in P4 are the "same kind" of proofs as in P3. (Dill has self-compiled C4 successfully also.)

McCarthy described the three main differences between C0 and C4 in a writeup. The second difference is the main source of improvement in the compiled code as well as the main reason for the length of P4.

(i) When the argument of CAR or CDR is a variable, C4 compiles a (HLR2@ 1 Ip) or (HRR2@ 1 Ip) which gets the result through the stack without first compiling the argument into an accumulator.

(ii) When C4 has to set up the arguments of a function in the accumulators, on general, C4 must compute the arguments one at a time and save them on the stack, and then load the accumulators from the stack, however, if one of the arguments is a variable, is a quoted expression, or can be obtained from a variable by a chain of CARs and CDRs, then it need not be computed until the time of loading accumulators since it can be computed using only the accumulator in which it is wanted.
CB computes Boolean expressions badly and generates many unnecessary labels and JRSTs. C4 is more sophisticated about this.

C4 uses four additional PDP-10 instructions: HLRZ#, HRRZ#, CAME, and CAMN. The first two are used, with the $\epsilon$-glon denoting indirect reference, to obtain CAR and CDR, respectively. A

assumption of P4 is that the instruction HLRZ# means \texttt{c(ac) - CAR(c(<ef>))} and that HRRZ# means \texttt{c(ac) - CDR(c(<ef>))}. Because CAR and CDR are compiled open rather than closed, as would be the case for an arbitrary function call, it must be explicitly emphasized that CAR and CDR of NIL, or numeric-atom are considered incorrect input. Since NULL and EQ are compiled open, the values of both must be explicitly defined for P4:

\[
V (\texttt{NULL EXP}) = T \text{ iff } V \text{ EXP } = \texttt{NIL}
\]

\[
V (\texttt{EQ EXP1 EXP2}) = T \text{ iff } V \text{ EXP1 } = V \text{ EXP2}
\]

with these definitions and motivation, the proof P4, organized in bottom-up style, follows.

The listings of the two compilers were checked by hand to discover the differences. The same set of differences was obtained when the listings were computer-compared by a file comparison utility program. These differences showed where new theorems were needed and where old proofs needed modification.

Lemma 4 [Definition of CCCHAIN(EXP)]. Assume EXP is a non-atomic expression, \texttt{CCCHAIN(EXP)} $= T$ if and only if \texttt{EXP} is of the form

\[
(CAR (CAR (\ldots (CAR a))))
\]

with at least one $\theta$. Each $\theta$ is either A or D (thus producing CAR or CDR) and a is an atom. In other words, \texttt{CCCHAIN(EXP)} $= T$ if and only if \texttt{EXP} is a car-cdr chain.

Proof. Induction on the number $N$ of leading $\theta$'s in EXP. Basis steps: If $N = 0$ then \texttt{CCCHAIN} gives NIL because CAR EXP is neither CAR nor CDR. If $N = 1$ then \texttt{EXP} = (CAR $a$). The result is T because CAR is CAR or CDR and $a$ is an atom. \texttt{CCCHAIN} a is not called.

Induction step: If \texttt{EXP} = (CAR1 (CAR2 (\ldots (CARN $a$))) with $N \geq 2$, then CAR$1$ is CAR or CDR so the left part of the AND is true, since $N \geq 2$, (CAR2 (\ldots (CARN $a$))) is not an atom, \texttt{CCCHAIN} may be invoked inductively, yielding T and hence \texttt{CCCHAIN EXP} gives T.

Lemma 5 [Definition of CLASS1(U, V)]. Input assumptions:

\[\text{U is a list of expressions (u1 u2 \ldots uN), V is an S-expression.}\]
Result. Let $cl$ be the classifying integer of $u_1$, namely

<table>
<thead>
<tr>
<th>Type</th>
<th>$cl$</th>
</tr>
</thead>
<tbody>
<tr>
<td>T, NIL8 numeric-atom</td>
<td>0</td>
</tr>
<tr>
<td>other atom</td>
<td>1</td>
</tr>
<tr>
<td>quoted expression</td>
<td>2</td>
</tr>
<tr>
<td>car-cdr chain</td>
<td>3</td>
</tr>
<tr>
<td>other expression</td>
<td>4</td>
</tr>
</tbody>
</table>

CLASS1($u, v$) = $((cN,uN),((c2,u2),((cl,u1),v)))$.

Induction step: CLASS1(CDR $u$, $((cl,u1),v))$ = $((cN,uN),((c2,u2),((cl,u1),v)))$. Note that $u_1$ in CCCHAIN $u_1$ is non-atomic since the first test for ATOM $u_1$ failed. For the special case $v = \text{NIL}$ the result reduces to the list of pairs $((cN,uN),((c2,u2),((cl,u1),v)))$.

Lemma 6 [Definition of CLASS2($u, v, \text{FLG}$)]. Input assumptions:

$u$ is a list of pairs $((cN,uN),((c2,u2),((cl,u1),v)))$ with $cl$ as defined in CLASS1, $v$ is an S-expression, FLG = T or NIL.

Result. Let $j$ be the greatest integer, if any, such that $cj = 4$ in $u._i$

<table>
<thead>
<tr>
<th>FLG</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>T</td>
<td>$((c1,u1),((c2,u2),((cN,uN),v)))$ with $cj$ now 5</td>
</tr>
<tr>
<td>NIL</td>
<td>$((c1,u1),((c2,u2),((cN,uN),v)))$ with $cj$ still 4</td>
</tr>
</tbody>
</table>

In words, the list of pairs is reversed and the first 4 is changed to 5.

Induction step: if-FLG = T and $cN = 4$ then CLASS2(CDR $u$, $((cN,uN),v)$, NIL) = $((c1,u1),((c2,u2),...,((5,uN),v))$ with $c1, c2, \ldots, cN=1$ as in $u$. if FLG $\neq T$ or $cN \neq 4$ then CLASS2(CDR $u$, $((cN,uN),v), \text{FLG}$) = $((c1,u1),((c2,u2),...,((cN,uN),v))$ with the $ci$'s as in the table of the result. Again, when $V = \text{NIL}$, the result reduces to the list of pairs $((c1,u1),((c2,u2),\ldots),(cN,uN))$.

Lemma 7 [Definition of CLASSIFY($u$)]. Assume $U = (u_1 u_2 \ldots u_N)$. Let $dl$ be the classifying integer of $u_1$ as in CLASS1 except the last other expression has $dl$ of 5 instead of 4. Then CLASSIFY($u$) = $((dl,u1),((d2,u2),\ldots),(dN,uN))$.

Proof. Composition of CLASS1 with $v$ as NIL and CLASS2 with $v$ as NIL and FLG as T. 

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Theorem 8 [Definition of COMPLIS(\(Z, M, K, VPR\))]. Input assumptions:

\(Z\) is a CLASSIFY'ed list of pairs \((dK, UK) (d[K+1], u[K+1]) \ldots (dN, uN)\).

Conditions D1-D7 of COMPLIS of Theorem 2.

Result. Let \(\texttt{e1, \ldots, e[\texttt{]}-1}\) denote those subscripts, if any, in \(Z\) for which \(d_1\) is equal to 4, and let \(\texttt{ej}\) denote the one \(d_i\), if any, equal to 5.

\[
\text{COMPLIS} = ((\text{Instructions to leave } V \texttt{ u[e1]} \text{ in } \texttt{ac1})
     \begin{align*}
     &\text{(PUSH P 1)} \\
     &\ldots \\
     &\text{(Instructions to leave } V \texttt{ u[e[\texttt{]}-1]} \text{ in } \texttt{ac1}) \\
     &\text{(PUSH P 1)} \\
     &\text{(Instructions to leave } V \texttt{ u[e]} \text{ in } \texttt{ac[e]})
     \end{align*}
\]

Note that this COMPLIS is a new function from that of Theorem 2. The function STACKUP(\(U, M, VPR\)) is identical to the old COMPLIS.

Proof. Structural Induction on \(Z\). Basis step: NULL \(Z\) gives NIL. Induction step: If \(dK = 4\) then \(\texttt{e1} = K\), COMPEXP(\(UK, M, VPR\)) inductively produces \((\text{Instructions to leave } V \texttt{ u[e1]} \text{ in } \texttt{ac1})\).

In view of the \(\text{(PUSH P 1)}\), then COMPLIS(((\(d[K+1], u[K+1]\)) \ldots (\(dN, uN\))), \(M-1, K+1, VPR\)) inductively completes the desired result.

If \(dK = 5\) then \(\texttt{ej} = K\) and there are no (more) 4's, COMPEXP(\(UK, M, VPR\)) inductively produces \((\text{Instructions to leave } V \texttt{ u[e]} \text{ in } \texttt{ac1})\).

If \(K = 1\) (i.e. \(\texttt{ej} = 1\)), no further instruction is needed nor generated because \(V \texttt{ u[e]}\) is already in \(\texttt{ac1}\). Otherwise if \(K \neq 1\), the instruction \((\text{MOVE } K 1))\) is generated to leave \(V \texttt{ u[e]}\) in \(\texttt{ac[e]} = \texttt{ac[K]}\).

If \(dK\) is neither 4 nor 5, COMPLIS(((\(d[K+1], u[K+1]\)) \ldots (\(dN, uN\))), \(M, K+1, VPR\)) inductively gives the desired result.

Theorem 9 [Definition of COMPC(EXP, N2, M, VPR)]. Input assumptions:

\(\text{EXP}\) is a car-cdr chain \((\texttt{C01R} (\texttt{C02R} \ldots (\texttt{C0N} \alpha)))\) where \(\texttt{N} \geq 1\); each \(\texttt{pi}\) is either \(A\) or \(D\); and \(\alpha\) is an atom \# T, NIL, numeric-atom.

Conditions C2-C6 and C7 for \(\alpha\) from COMPEXP of Theorem 1.

Result. \(\text{COMPC} = ((\texttt{ac[N2]} := \texttt{C01R} \texttt{ac[N2]}) \\
\quad (\texttt{ac[N2]} := \texttt{C02R} \texttt{ac[N2]}) \quad \alpha)\)
(ac[N2] := CPNR α))

Only accumulator N2 is used,

Proof, Induction on the number J of β's in EXP, Define 61 to be L or R according as β is A or D, Basis step: If N = 1 then EXP = (CβIR α). Since ATOM α, COMPC produces

(((Hε1IRζ N2 M+CDR ASSOC(α, VPR) P))

which is (ac[N2] := CβIR α), the last line of the result.

Induction step: If N > 2 then NOT ATOM (Cβ2R (..., (CPNR α))). Hence COMPC produces

(Hε1IRζ N2 N2)

. COMPC((Cβ2R (..., (CPNR α))), N2, M, VPR)

which, invoking COMPC inductively, becomes

(((ac[N2] := CβIR ac[N2] 3)

(ac[N2] := Cβ2R ac[N2])

(ac[N2] := CPNR α))

Incidentally, the assumption that EXP is a car-cdr chain makes unnecessary the error check at the first line of COMPC.

Theorem 10 [Definition of LOADAC(Z, M2, N2, M, VPR)], Input assumptions:

Z is a CLASSIFY'ed list of pairs.

Z = ((d[N2], u[N2]) (d[N2+1], u[N2+1]) ... (dN, uN))

Conditions 01-07 of COMPLEIS of Theorem 2.

Let e1, e2, ..., e[q1-M2] denote those subscripts, if any, in Z for which d[j] is equal to 4. The stack P contains the values of the 1-M2 u[e[j]]'s as follows

P | V u[e1] V u[e2] ... V u[q-M2]

Let ej, with j > q-M2, denote the one, if any, equal to 5. Assume ac[e[j]] holds V u[e[j]].

Result, LOADAC = ((Instructions to leave V u[N2] in ac[N2])

(Instructions to leave V u[N2+1] in ac[N2+1])

...)

(Instructions to leave V UN in acN))

Each line of instructions uses only the accumulator mentioned. The stack P is unaltered. (The ej-th line involving ac[e[j]] is missing.)

Proof, Structural induction on Z. Basis step: NULL Z gives NIL. Induction step: Six cases based on the classifying integer d[N2]. If d[N2] = 1 then u[N2] is an atom. LOADAC produces

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(MOVE N2 M+CDR ASSOC(u[N2], VPR) P)
  LOADAC((d[N2+1].u[N2+1]) ... (dN,uN)), M2, N2+1, M, VPR)

The MOVE instruction leaves V u[N2] in ac[N2] using only ac[N2]. Inductively the LOADAC part completes the result including the unalteration of the stack. The use of the infix dot follows the conventions that the value of LOADAC is a list of instructions.

If d[N2] = 0 or 2 then u[N2] is either T,NIL, or numeric-atom or a quoted expression. The proofs are each similar to the case d[N2] = 1. The generated instructions are, respectively,

(MOVE1 N2 (QUOTE u[N2]))

and

(MOVE1 N2 u[N2])

with each followed by the same LOADAC term as in the first case. Both MOVEi instructions leave V u[N2] in ac[N2] using only ac[N2], and again the LOADAC term inductively completes the result.

If d[N2] = 3 then u[N2] is a car-cdr chain. Syntactically correct input implies the atom a at the end of the chain is neither T,NIL, nor numeric-atom. Thus COMPC may be invoked. Since a car-cdr chain is executed from right to left, the REVERSE function is needed. LOADAC Produce

[((ac[N2] := CDR a)
  ...
  (ac[N2] := 'C32R ac[N2])
  (ac[N2] := ~ C01R ac[N2])
  (same LOADAC term as first case))

The first N lines are

(Instructions to leave V u[N2] in ac[N2])

and the LOADAC term inductively completes the result.

If d[N2] = 5 then ac[N2] is not altered. LOADAC(((d[N2+1].u[N2+1]) ... (dN,uN)), I, N2+1, M, VPR) Inductively gives the result. (The constantias the second argument in this call to LOADAC means 1-M2 = 1-1 = 0, i.e., the stack input condition of LOADAC is vacuous.)

Finally, if d[N2] = 4 then the last test of LOADAC produces

(MOVE N2 M2 P)

which, using only ac[N2], leaves V u[N2] in ac[N2] because there are 1-M2 = -M2+1 of the (V u[el])'s in the stack.

LOADAC(((d[N2+1].u[N2+1]) ... (dN,uN)), M2+1, N2+1, M, VPR)
inductively completes the result since there is now one fewer 4 in the remaining \(d[N+2], \ldots, dN\). Even though the stack is unaltered, the stack segment of interest is now from \(V_u[e2] \) to \(V_u[-M2] \) which the stack input condition inductively renumbers as \(V_u[e1] \) to \(V_u[-M2] \).

**Lemma 8** (Definition of \(C\)). Assume \(Z\) is a CLASSIFIED list of pairs \((d1,u1),(d2,u2),\ldots,(dN,uN)\). \(C\) gives the number of \(d\)'s that are 4. This number is denoted by \#4.

**Proof.** Structural induction on \(Z\), Basis step: \(NULL\, Z\) gives 0. Induction step: If \(d1 = 4\) then \(1 + C\) gives the result. If \(d1 \neq 4\) then \(C\) inductively gives the result.

**Lemma 9.** If \(N \geq 0\) then \(SUB Stack N\) is the same function as \(LIST LIST('SUB', P, LIST('C', 0, 0, N, N))\).

**Proof.** If \(N = 0\) then \(NIL\) is \(LIST LIST('SUB', P, LIST('C', 0, 0, 0, 0))\). If \(N > 0\) then it is clear.

**Theorem 11** (Definition of \(COMPLISA(U, M, VPR)\), Input assumptions:

\(U = (u1, u2, \ldots, uN)\) is a list of arguments,

Conditions D2-D7 of COMPLISA of Theorem 2,

**Result.** \(acl\) holds \(V_u1\) for \(1 \leq i \leq N\). The stack \(P\) is safe over the output of COMPLISA.

**Proof.** COMPLISA(CLASSIFY \(U, M, 1, VPR\)) places the class 4 arguments on the stack in the order required for \(LOADAC\). COMPLISA also leaves the class 5 argument, say \(uI, I_{acj}\), in \(acl\). It is permissible to invoke

\[LOADAC(((d1,u1),(d2,u2),\ldots,(dN,uN)),1-M4,1,M-M4,VPR)\]

since (i) there are now \(-(M-M4) = -M+M4\) accessible stack locations, (ii) there are 1-\((1-M4) = #4\) of the \(d\)'s which are 4, (iii) the stack \(P\) contains the class 4 arguments in the proper order by the result of COMPLISA, and (iv) \(acj\) holds \(V_uJ\) by the last line of the result of COMPLISA. After SUBSTACK\#4, the result is established.

The order of first COMPLISA and then \(LOADAC\) avoids the need to stack a non-class 4 argument since after the class 5 argument is computed by COMPLISA, \(LOADAC\) may assume the safety of all \(acj\), \(1 \leq i \leq N2\).

**Theorem 12** (Definition of \(COMPANDOR1(U, M, L, L2, FLG, VPR)\), Input assumptions:
\[ U = (u_1, u_2, \ldots, u_N), \]
Conditions E1-E9 of COMPANDOR of Theorem 4.
L2 is a label different from L.

Result. COMPANDOR produces a list \( I \) of instructions given by

\[
\begin{align*}
\text{FLG} & \quad \text{Algorithm equivalent of } I \\
\text{NIL} & \quad \text{If NOT } u_1 \text{ then go to } L_1 \\
& \quad \text{If NOT } u_2 \text{ then go to } L_1 \\
& \quad \cdots \\
& \quad \text{If NOT } u_{[N-1]} \text{ then go to } L_1 \\
& \quad \text{If } u_N \text{ then go to } L_2; \\
\text{T} & \quad \text{If } u_1 \text{ then go to } L_1 \\
& \quad \text{If } u_2 \text{ then go to } L_1 \\
& \quad \cdots \\
& \quad \text{If } u_{[N-1]} \text{ then go to } L_1 \\
& \quad \text{If NOT } u_N \text{ then go to } L_2; \\
\end{align*}
\]

If, however, \( U \) is NULL then the Algorithm equivalent produced is "go to L2;" The statements labeled \( L \) and \( L_2 \) are not in \( I \). \( P \) is unsafe over the execution of \( I \).

Proof. Structural Induction on \( U \). NULL \( U \) gives "go to \( L_2;\)."

Induction step: Assume \( \text{FLG} = T \). If NULL \((u_2, \ldots, u_N)\), i.e. \( N = 1 \), then

\[
\text{COMPANDOR} = \text{COMBOOL}(u_1, M, L_2, \text{NIL}, VPR)
\]
\[
= \text{if NOT } u_1 \text{ then go to } L_2;
\]
as required, if NOT NULL \((u_2, \ldots, u_N)\), i.e. \( N \geq 2 \), then

\[
((\text{COMBOOL}(u_1, M, L, \text{FLG}, VPR))
\quad ((\text{COMPANDOR}((u_2, \ldots, u_N), M, L, L_2, \text{FLG}, VPR)))
\]

inductively gives the result. Note that \((u_2, \ldots, u_N)\) is not NULL in the Inductive call. The uniqueness of the Label generation mechanism will help show that the labels \( L \) and \( L_2 \) are outside \( I \). The case \( \text{FLG} = \text{NIL} \) is essentially identical.

Theorem 13 (Definition of \( \text{COMBOOL}(P, M, L, \text{FLG}, VPR) \)). Input assumptions are the same as \( \text{COMBOOL} \) of Theorem 5. \( \text{COMBOOL} \) produces a list \( I \) of instructions given by (the same as Theorem 5)

\[
\begin{align*}
\text{FLG} & \quad \text{Algorithm equivalent of } I \\
\text{NIL} & \quad \text{If NOT } P \text{ then go to } L; \\
\text{T} & \quad \text{If } P \text{ then go to } L;
\end{align*}
\]
with the statement labeled L not in I, P is safe over the execution of I.

Proof. (Modifications to the proof of theorem 5.) Assume FLG = T. Add a case $P = T$ which from case 0.1 produces $(\text{JRST} \emptyset \ L)$ as required. Add a case $P = (\text{EQ} \alpha \beta)$ with $\alpha$ and $\beta$ expressions. Inductively invoke COMPLISA($\alpha \beta$, M, VPR). COMBOOL produces from case 1.1

$$(((\text{ac1 holds V } \alpha)\\(\text{ac2 holds V } \beta)\\(\text{CAMN } 1 \ 2)\\(\text{JRST } \emptyset \ L)))$$

$= (\text{if } (\text{EQ } \alpha \beta) \text{ then go to } L1)$

$= (\text{if } P \text{ then go to } L1)$

Modify the $P = (\text{AND } \alpha)$ case. If $\alpha$ is non-NULL then after evaluating COMBANDOR1($\alpha$, M, L1, L, NIL, VPR), the result follows by noting the equivalence of

$$(((\text{If NOT uN then go to } L1;)\\(\text{JRST } L)\\L1))$$

and

$$(((\text{if uN then go to } L1;)\\L1)$$

If $\alpha$ is NULL, than $((\text{JRST } L) \ L1)$ results in both instances.

Under the assumption FLG = T, the $P = (\text{OR } \alpha)$ case is unchanged.

Add the case $P = (\text{NULL } \alpha)$ with $\alpha$ an expression, COMBOOL produces from case 4.1

$$(((\text{COMPEXP}(\alpha, M, VPR))\\(\text{JUMPE } 1 \ L)))$$

$= (((\text{Instructions to leave V } \alpha \ \text{lnac1})\\(\text{JUMPE } 1 \ L)))$

$= (\text{if } P \text{ then go to } L1)$

These cases with FLG = NIL are proved similarly. The tests in COMBOOL are slightly different: T is treated separately rather than as an atom; the EQ and NULL functions are treated separately rather than as arbitrary functions in the last test. These differences do not affect the result of COMBOOL.

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Theorem 14 [Definition of COMCOND(U, M, L, VPR)]. Same as COMCOND of Theorem 6.

Proof. To the proof of Theorem 6 add two cases to the induction step corresponding to the second and third tests of COMCOND. The second test asks if the pair (u1 u2) is the pair ((NULL α) NIL). If so COMCOND produces

\[
((\text{COMPEXP}(\alpha, M, VPR))
\begin{align*}
&\text{JUMPE 1 L} \\
&\text{COMCOND}((u3 \ldots u[2N-1] u[2N]), M, L, VPR))
\end{align*}
\]

\[
= ((\text{Instructions to leave V α in ac1})
\begin{align*}
&\text{JUMPE 1 L} \\
&\text{ac1} := \text{if } u3 \text{ then } u4 \ldots \text{ else if } u[2N-1] \text{ then } u[2N] \text{ L})
\end{align*}
\]

by inductively invoking COMPEXP and COMCOND

\[
= (\text{ac1} := \text{if NULL α then NIL else if u3 then u4 \ldots else if } u[2N-1] \text{ then } u[2N] \text{ L})
\]

by checking two cases on NULL at if NULL α than ac1 already holds \(\emptyset = V \text{ NIL} \).

The third test asks if (u1 u2) is (T u2). If so any succeeding pairs may be ignored. COMCOND produces

\[
((\text{COMPEXP}(u2, M, VPR))
\begin{align*}
&\text{L})
\end{align*}
\]

as required.

Theorem 15 [Definition of COMPEXP(EXP, M, VPR)]. Same as Theorems 1 and 7.

Proof. (Modifications to the proofs of Theorems 1 and 7.) Add a case for \(\text{EXP} = (\text{CAR} \alpha)\), by correct syntax, \(\alpha \neq T, \text{NIL, numeric-atom}\), if \(\alpha\) is an atom case 3.1a produces

\[
\text{HLRZ} 1 \text{ M+CDR ASSOC(α, VPR)}
\]

As in Theorem 1, case 3, M+CDR ASSOC(α, VPR) is correct; by the definition of HLRZ, ac1 holds V EXP. If \(\alpha\) is not an atom, then case 3.1b holds. Invoking COMPEXP(α, M, VPR) inductively leaves V \(\alpha\) in ac1, from which (HLRZ 1 I I) produces CAR V \(\alpha\) = V EXP in ac1 as required. The additional case for \(\text{EXP} = (\text{CDR} \alpha)\) is identical to the case for CAR except for HRRZ.

Case 4, The first case of Theorem 7 also handles the function EQ since Theorem 13 handles EQ.

Case 7, \(\text{EXP} = (\text{fname} \alpha)\) where \(\alpha\) consists of N arguments, COMPEXP produces
This is incorrect, i.e. acl holds V EXP in view of the definitions of COMPLISA and CALL,

Case 8, STACKUP is identical with COMPLIS of Theorem 2. Use Lemma 9 on SUBSTACK.

Theorem 16 (Correctness of the compiler), Same as Theorem 3,

Proof, Same as Theorem 3 but using Lemma 9.

Termination of C4 follows by essentially the same argument as used for C0, CLASSIFY and SUBSTACK join COMP as exceptions since neither is recursive. COMPLISA can be shown to terminate by replacing its two calls (in COMPEXP, case 7 and COMBOOL, case 1) by the body of COMPLISA; this substitution will allow the body to reference substructures directly. This completes the proof P4 of the compiler C4.

The process of constructing P4 uncovered six errors in C4 as originally written. In addition to the numeric-atom problem in C0, three were found early on by attempting to show that CARs and CDRs in C4 were always well-defined, i.e. not applied to atoms. Although no further errors were expected, the other three surfaced after carefully stating the theorems and then discovering where the proof could not be completed. Each case that failed led very quickly to the construction of a counter-example to the statement of correctness, and furthermore showed what changes to C4 would be sufficient. These changes were made (by London) and the proof was completed.

The changes made to C4 are shown in the listing of the compiler in Appendix 2. Each change is now elaborated!

(i) COMPEXP, case 2, Same change to C0 for numeric-atoms.

(ii) COMCOND, line 2 and COMBOOL, case 1, Found by checking CARs and CDRs for being well-defined. Counter-examples are Boolean atomic variables.

(iii) COMPANDOR, lines 1-2, Found as in (ii), Only counter-examples are (AND) and (OR). Incorrectness in the first proposed change [IF NULL U THEN NIL ELSE], which seems correct, was only discovered by checking the case N = 0 in P = (AND a) of Theorem 13.

(iv) LOADAC, case CAAR Z = 0 and CLASS1, lines 3-5, Found by considering the case of T, NIL, and numeric-atoms as actual parameters to a function in the atom case for LOADAC in Theorem 10.
(v) LOADAC, case CAAR z = 5. Found by noting that the result for LOADAC in Theorem 10 did not inductively follow if d[N2] = 5. Counter-examples are function calls with a class 5 argument; all succeeding arguments failed to be compiled at all.

(vi) COMBOOL, case 5. Found by reconsidering the case of a LAMBDA expression in Boolean context (for example an argument to AND, OR, or COND) at the last case of Theorem 5 which case failed in Theorem 13.

As a check on the changes and the completed proof P4, London used the changed C4 to compile some of McCarthy's test functions and also a set of representative counter-examples. The test functions gave identical output as the original C4 (another use of the file comparison utility program). The counter-examples gave correct output as determined by a hand inspection.

ACKNOWLEDGMENTS

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REFERENCES


Heim, A. C., 1973, REDUCE 2 user's manual 8 Artificial Intelligence Memo AIM-133, Stanford University, October.


Lindon, R. L., 1972, Proving programs correct: Some techniques and examples, BIT, 10, 2, pp. 168-182.


Weissman, C., 1967, Lisp 1.5 Primer, Dickenson Publishing Co.

Wirth, N., 1971, Program development by stepwise refinement, Comm. ACM, 14, 4, April, pp. 221-227.
APPENDIX 1 - A LISTING OF THE COMPILER CO

FEXPR COMPL FILE ← BEGIN SCALAR Z;
EVAL('OUTPUT ,('DSK: , LIST (CAR FILE, 'LAP)))$
EVAL('INPUT ,('DSK: , FILE))$
INC('T ,NIL)$
OUTC(T,NIL)$

LOOP: Z ← ERRSET(READ())$
IF ATOM Z THEN GO TO DONE$
Z ← CAR Z$
IF CAR Z EQ 'DETHEN
BEGIN SCALAR PROC;
   PROG ← COMP(CADR Z, CADDR Z, CADDDR Z)$
   MAPC(FUNCTION(PRINT), PROG)$
   OUTC(NIL,NIL)$
   PRINT LIST(CADR Z, LENGTH PROG)$
   OUTC(T,NIL)$
END
ELSE PRINT Z$
GO TO LOOP$
DONE: OUTC(NIL,T)$
INC(NIL,T)$
RETURN ENDCOMPEND;

******************************************************************************
For the purposes of this paper, the compiler starts here; above here
may be ignored.
******************************************************************************

COMP(FN,VARS,EXP) ←
   (LAMBDA N;
   APPEND
      LIST LIST('LAP, FN, 'SUBR),
      MKPUSH(N,1),
      COMPEXP(EXP, -N, PRUP(VARS, 1)),
      LIST LIST ('SUB , 'P, LIST ('C, 0, N, N)),
      '((POPJ P) NIL)))

LENGTH VARS;
PRUP(VARS,N) ← IF NULL VARS THEN NIL
   ELSE (CAR VARS * N) . PRUP(CDR VARS, N+1);
MKPUSH(N,M) ← IF NK M THEN NIL ELSE LIST('PUSH , 'P , M). MKPUSH(N, M+1);

COMPEXP(EXP,M,VPR) ←
[1] IF NULL EXP THEN '(' (MOVEI 1 0))
[2] ELSE IF EXP EQ 'T OR NUMREP EXP THEN
       LIST LIST('MOVEI, 1,(LIST('QUOTE, EXP)))
[3] ELSE IF ATOM EXP THEN
       LIST LIST('MOVE ,1,M+CDR ASSOC(EXP,VPR), 'P )
[4] ELSE IF CAR EXP EQ 'AND OR OR CAR EXP EQ 'OR OR
       C A R EXP EQ 'NOT THEN
(LAMBDA L1,L2; APPEND(COMBOOL(EXPR,M,L1,NIL,VPR),
  LIST(('MOVEI 1 (QUOTE T)),LIST('JRST 0,L2),
  L1,('MOVEI 1 0)L2)))

(GENSYM(),GENSYM())

[5]    ELSE IF CAR EXP EQ 'COND THEN
  COMCOND(CDR EXP,M,GENSYM(),VPR)

[6]    ELSE IF CAR EXP EQ 'QUOTE THEN LIST LIST('MOVEI,1,EXP)

[7]    ELSE IF ATOM CAR EXP THEN
  (LAMBDA N1; APPEND(COMPLIS(CDR EXP,M,VPR),
    LOADAC(1-N1,1),
    LIST LIST('SUB ,P ,LIST('C,0,0,N,N)),
    LIST LIST('CALL ,N,
    LIST('E,CAR EXP)))))

LENGTH CDR EXP;

COMPLIS(U,M,VPR) =
  IF NULL U THEN NIL
  ELSE APPEND(COMPEXP(CAR U,M,VPR),
    '((PUSH P 1)),
    COMPLIS(CDR U,M-1,VPR));

LOADAC(N,K) = IF N>0 THEN NIL ELSE LIST('MOVE ,K,N,'P ),
  LOADAC(N+1,K+1);

COMCOND(U,M,L,VPR) =
  IF NULL U THEN LIST L
  ELSE (LAMBDA L1; APPEND(
    COMBOOL(CAAR U,M,L1,NIL,VPR),
    COMEXP(CADDAR U,M,N,
    APPEND(PRUP(CADDAR U,1-M),VPR)),
    LIST LIST('SUB ,P ,LIST('C,0,0,N,N)))
  LENGTH CDR EXP;

COMBOOL(P,M,L,FLG,VPR) =
[1]    IF ATOM P THEN APPEND(COMPEXP(P,M,VPR),
      LIST LIST(IF FLG THEN 'JUMPN
      ELSE 'JUMPE ,1,L))

[2] ELSE IF CAR P EQ 'AND THEN
[3] ELSE IF CAR P EQ 'OR THEN
  ELSE (LAMBDA L1; APPEND(
    COMBOOL(CDR P,M,L1,NIL,VPR),
    LIST LIST('JRST ,L1),
    LIST L1))
  GENSYM());
[6]
ELSE (LAMBDA L1; APPEND(
  COMPANDOR(CDR P, M, L1, T, VPR),
  LIST LIST('JRS, T, L),
  LIST L1)))

[4] ELSE IF CAR P = Q 'NOT THEN
  COMBOOL(CADR P, M, L, NOT FLG, VPR)
[5] ELSE APPEND(COMPEXP(P, M, VPR),
  LIST LIST(IF FLG THEN 'JUMPN
  ELSE 'JUMPE 1, L));

COMPANDOR(U, M, L, FLG, VPR)  *  IF NULL U THEN NIL
  ELSE APPEND(COLOBL(CAR U, M, L, FLG, VPR),
    COMPANDOR(CDR U, M, L, FLG, VPR));
APPENDIX 2 - ALISTING OF THE MORE OPTIMIZING COMPILER C4

The changes needed to complete the proof of correctness of C4 are shown in this listing. Relations enclosed between the symbols < and > and additions enclosed between the symbols [ and ] with the latter two also being used to number cases. The eight changes are at COMPEXP, case 2; COMCONDO, line 2; LOADAC, cases CAAR Z = 0 and CAARZ 5; CLASS1, lines 3-5; COMBOOL, cases 1 and 5; and COMPANDOR1, lines 1-2:

```lisp
(FEXPR CGMPL FILE* BEGINSCAL AR2;
   EVAL('*OUTPUT , (DSK: LIST (CAR FILE , 'LAP)));
   EVAL('*INPUT , (DSK: FILE));
   INC('T , NIL) ;
   CUTC (T , NIL) ;
   LOOP: Z = ERRSET (READ());
      IF ATOM Z THEN GOTO O DONE;
      Z = CAR Z;
      IF CAR Z EC? DE THEN
   BEGINS C AL AR PROG;
      PROG = COMP (CAAR Z , CADR Z , CADJ DR Z ) ;
      MAPC (FUNCTION (PRINT), PROG) ;
      OUTC (NIL, NIL) ;
      PRINT LIST (CAAR Z , LENGTH PROG) ;
      OUTC (T , NIL) ;
   END
      ELSE
      PRINT Z ;
      GOTO LOOP ;
   DONE: OUTC (NIL , T) ;
      INC (NIL , T) ;
      RETURN 'END COMP END ;
```

For the purposes of this paper, the compiler starts here; above here may be ignored.
PRUP(VARS,N) ← IF NULL VARS THEN NIL
    ELSE (CAR VARS, N), PRUP(CDR VARS, N+1)

MKPUSH(N,M) ← IF N<M THEN NIL ELSE LIST('PUSH,'P,M), MKPUSH(N,M+1)

COMPEXP(EXP,M,VPR) ←
[1]   IF NULL EXP THEN '((MOVEI 1 ø))
[2]   ELSE IF EXP EQ 'T THEN '((MOVEI 1 (QUOTE T)))
[3]   ELSE IF ATOM EXP THEN
[3,1]   LIST LIST('MOVE ,1,M+CDR ASSOC(EXP,VPR), 'P)
[3,2]   ELSE IF CAR EXP EQ 'CAR THEN
[3,2,1]   IF ATOM CAAR EXP THEN
[3,2,2]   LIST LIST('HLRZ@1, M+CDR ASSOC(CADR EXP,VPR), 'P)
[3,2,3]   ELSE APPEND(COMPEXP(CADR EXP,M,VPR),
[3,2,4]   '((HLRZ@1 1))'))
[4]   ELSE IF CAR EXP EQ 'CDE THEN
[4,1]   IF ATOM CAAR EXP THEN
[4,2]   LIST LIST('HRRZ@1, M+CDR ASSOC(CADR EXP,VPR), 'P)
[4,3]   ELSE APPEND(COMPEXP(CADR EXP,M,VPR),
[4,4]   '((HRRZ@1 1))'))
[5]   ELSE IF CAR EXP EQ 'AND OR CAR EXP EQ 'OR OR
[6]   ELSE IF CAR EXP EQ 'NOT OR CAR EXP EQ 'EQ THEN
(LAMBDA L1,L2; APPEND(
    COMBOOL(EXP,M,L1,NIL,VPR),
    LIST('MOVEI 1 (QUOTE T)),LIST('JRST 0,L2),
    L1,'(MOVEI 1 0),L2))
[7]   ELSE IF CAR EXP EQ 'COND THEN
[8]   ELSE IF CAR EXP EQ 'QUOTE THEN LIST LIST('MOVEI,1,EXP)
[7]   ELSE IF ATOM CAR EXP THEN
[8]   APPEND(COMPLISA(CDR EXP,M,VPR),
    LIST('CALL LENGTH CDR EXP,
    LIST('E ,CAR EXP)))
[8]   ELSE IF CAAR EXP EQ 'LAMBD A THEN
(LAMBD A N; APPEND(STACKUP(CDR EXP,M,VPR),
    COMPEXP(CADDAR EXP,M-N, APPEND(PRUP(CADAR EXP,1-M),VPR)),
    SUBSTACK N))
    LENGTH CDR EXP)

STACKUP(U,M,VPR) ← IF NULL U THEN NIL
    ELSE APPEND(COMPEXP(CAR U,M,VPR),
    '((PUSH P 1)),
    STACKUP(CDR U,M-1,VPR));
CCCHAIN EXP = (CAREXP EQ CAR OR CAREXP EQ CDR) AND
(ATOM CAUR EXP OR CCCHAIN CADR EXP);

COMPC(EXP,N2,M,VPR) ->
IF ATOMEXP THEN ROR COMPC
ELSE IF CAR EXP EQ CAR THEN
(IF ATOM CAUR EXP THEN
LIST LIST('HLRZ# ,N2,M+CDR ASSOC(CADR EXP,VPR),'P )
ELSE LIST('HLRZ# ,N2,N2),COMPC(CADR EXP,N2,M,VPR))
ELSE IF ATOMCADR EXP THEN
LIST LIST('HRRZ# ,N2,M+CDRASSOC(CADR EXP,VPR),'P )
ELSE LIST('HRRZ# ,N2,N2),COMPC(CADR EXP,N2,M,VPR);

COMCOND(U,M,L,VPR) ->
IF NULL U THEN LIST L
ELSE IF NOT ATOM CAAR AND
CAAR = NULL AND NULL CADAR THEN
APPEND(COMPEXP(CAAR U,M,VPR),
LIST LIST('JUMPE ',1,L),
COMCOND(CDR U,M,L,VPR))
ELSE IF CAAR EQ THEN
APPEND( COMPEXP(CADR U,M,VPR),LIST L)
ELSE (LAMBDA L1; APPEND(
COMCOND(CAAR U,M,L1,NIL,VPR),
COMPEXP(CADR U,M,VPR),
LIST(LIST('JUKE ',0,L),L1),
COMCOND(CDR U,M,L,VPR))
GENSYM);

COMPLISA(U,M,VPR) ->
(LAMBDA Z; APPEND(
COMPLIS(Z,M1,VPR),
LOADAC(Z,1-COUNT Z,1,M-COUNT Z,VPR),
SUBSTACK(COUNT 1))
CLASSIFY U;

COUNT Z = IF NULL Z THEN U ELSE IF CAAR Z = 4 THEN 1+COUNT CDR Z
ELSE COUNT CDR Z;

LOADAC(Z,M2,N2,M,VPR) ->
IF NULL Z THEN NIL
ELSE IF CAAR Z = 1 THEN
LIST('MOVE, N2,M+CDRASSOC(CADR Z,VPR),'P )
LOADAC(CDR Z,M2,N2+1,M,VPR)
ELSE IF CAAR Z = 3 THEN
LIST('MOVIE, N2, (LIST('QUOTE, CDAR Z) )
LOADAC(CDR Z,M2,N2+1,M,VPR)]
ELSE IF CAAR Z = 2 THEN
LIST('MOVIE, N2,CDAR Z)
LOADAC(CDR Z,M2,N2+1,M,VPR)
ELSE IF CAAR Z = 3 THEN

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APPEND(REVERSE COMPC(CAR P, N2, M, VPR),
LOADA(CDR P, N2+1, M, VPR))
ELSE IF CAAR P = 5 THEN <NIL> [LOADA(CDR P, 1, N2+1, M, VPR)]
ELSE LIST('MOVE, N2, M2, 'P),
LOADA(CDR P, M2+1, N2+1, M, VPR))

COMPLIS(Z, M, K, VPR) =
IF NULL Z THEN NIL
ELSE IF CAAR Z = 4 THEN APPEND(,
COMPEXP(CDR Z, M, VPR),
'((PUSH P 1)),
COMPLIS(CDR Z, M-1, K+1, VPR))
ELSE IF CAAR Z = 5 THEN APPEND(,
COMPEXP(CDR Z, M, VPR),
IF K=1 THEN NIL
ELSE LIST LIST('MOVE, K, 1))
ELSE COMPLIS(CDR Z, M, K+1, VPR))

CLASSIFY U = CLASS2(CLASS1(U, NIL), NIL, T):

CLASS1(U, V) = IF NULL U THEN V
ELSE IF ATOM CAR U THEN
  [(IF CAR U = 'NIL OR CAR U = 'T OR NUMBERP CAR U THEN
   CLASS1(CDR U, (0, CAR U), V)
   ELSE] CLASS1(CDR U, (1, CAR U), V)[)
ELSE IF CAAR U = 'QUOTE THEN CLASS1(CDR U, (2, CAR U), V)
ELSE IF CCCHAIN CAR U THEN CLASS1(CDR U, (3, CAR U), V)
ELSE CLASS1(CDR U, (4, CAR U), V))

CLASS2(U, V, FLG) = IF NULL U THEN V
ELSE IF FLG AND (CAAR U = 4) THEN
  CLASS2(CDR U, (5, CDAR U), V, NIL)
ELSE CLASS2(CDR U, CAR U, V, FLG))

MKJRST L = LIST LIST('JRST, 0, L))

COMSOOL(P, M, L, FLG, VPR) =
[0.1] IF P EQ 'T THEN (IF FLG THEN MKJRST L ELSE NIL)
[1.3] ELSE IF ATOM P THEN APPEND(,
COMPEXP(P, M, VPR),
LIST LIST(IF FLG THEN 'JUMPN
ELSE 'JUMPE, 1, L))]

[1.1] ELSE IF CAR P EQ 'EQ THEN APPEND(,
COMPLISA(CDR P, M, VPR),
IF FLG THEN '((CAMN 1 2)) ELSE '((CAME 1 2)),
MKJRST L)

[2] ELSE IF CAR P EQ 'AND THEN
[6] ELSE (LAMBDA L1; APPEND(,
COMPANDOR1(CDR P, M, L1, L, NIL, VPR),
LIST L1))
GENSYM())
(3) \text{else if car p eq 'or then}

(a) \text{(if flg then compandor(car p, m, l, t, vpr)}

(b) \text{else (lambda l1; append(}

\text{compandor1(car p, m, l1, l, t, vpr),}

\text{list l1))}

\text{gensym()}

(4) \text{else if car p eq 'not then}

\text{compool(car p, m, l, not flg, vpr)}

(4.1) \text{else if car p eq 'null then append(}

\text{complex(car p, m, vpr),}

\text{list list(if flg then 'jumpe}

\text{else 'jumpl,1,l))}

(5) \text{else if atom car p then append(}

\text{complex(car p, m, vpr),}

\text{list list(if flg then 'jumpe}

\text{else 'jumpl,1,l));}

\text{compandor(car u, m, l, flg, vpr) = if null u then nil}
\text{else append(combool(car u, m, l, flg, vpr),}
\text{compandor(car u, m, l, flg, vpr))}.

\text{compandor1(car u, m, l, l2, flg, vpr) = [if null u then mkirst l2}
\text{else if null cdr u then compool(car u, m, l2, not flg, vpr)}
\text{else append(combool(car u, m, l2, flg, vpr),}
\text{compandor1(car u, m, l, l2, flg, vpr))];}
Code from C0

(LAP REV SUBR)
(PUSH P 1)
(PUSH P 2)
(MOVE 1 -1 P)
(PUSH P 1)
(MOVE i 0 P)
(SUB P (C 0 0 i 1))
(CALL 1 (E NULL))
(OUTPUT 1 L2)
(MOVE 1 0 P)
(JRST L1)

L2
(MOVE 1 (QUOTE T))
(JUMPE 1 L3)
(MOVE 1 -1 P)
(PUSH P 1)
(MOVE 1 0 P)
(SUB P (C 0 0 i 1))
(CALL 1 (E CAR))
(PUSH P 1)
(MOVE 1 -2 P)
(PUSH P 1)
(MOVE 1 -1 P)
(MOVE 2 0 P)
(SUB P (C 0 0 2 2))
(CALL, 2 (E CONS))
(PUSH P 1)
(MOVE 1 -1 P)
(MOVE 2 0 P)

L1
(SUB P (C 0 0 2 2))
(POP P)
NIL

Comments
header
stack first arg
stack second arg
compute X
stack it
recall X
adj. stack by 1
if not NULL jump
recall X
jump for return
the label L2
compute T
if not T jump
compute X
recall X
CDR
compute X
recall X
CAR, resp. CAR X
compute Y
recall CAR X
recall Y
adj. stack by 2
CONS.
recall CDR X
recall CONS, resp. transfer CONS
compute CDR X
REV
jump for return
return
end of code

Code from C4

(LAP REV SUBR)
(PUSH P 1)
(PUSH P 2)

(MOVE 1 -1 P)

(JUMP 1 L2)
(MOVE 1 0 P)
(JRST L1)

L2

(HLR@ 1 -1 P)

(MOVE 2 0 P)

(CALL 2 (E CONS))

(MOVE 2 1)

(HRR@ 1 -1 P)

(CALL 2 (E REV))

L1
(SUB P (C 0 0 2 2))
(POP P)
NIL